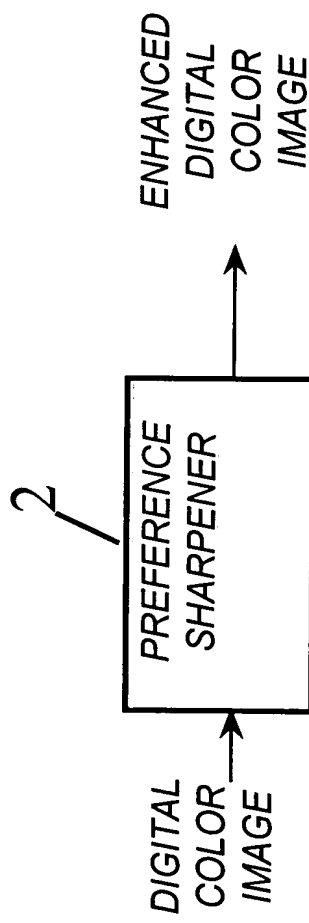


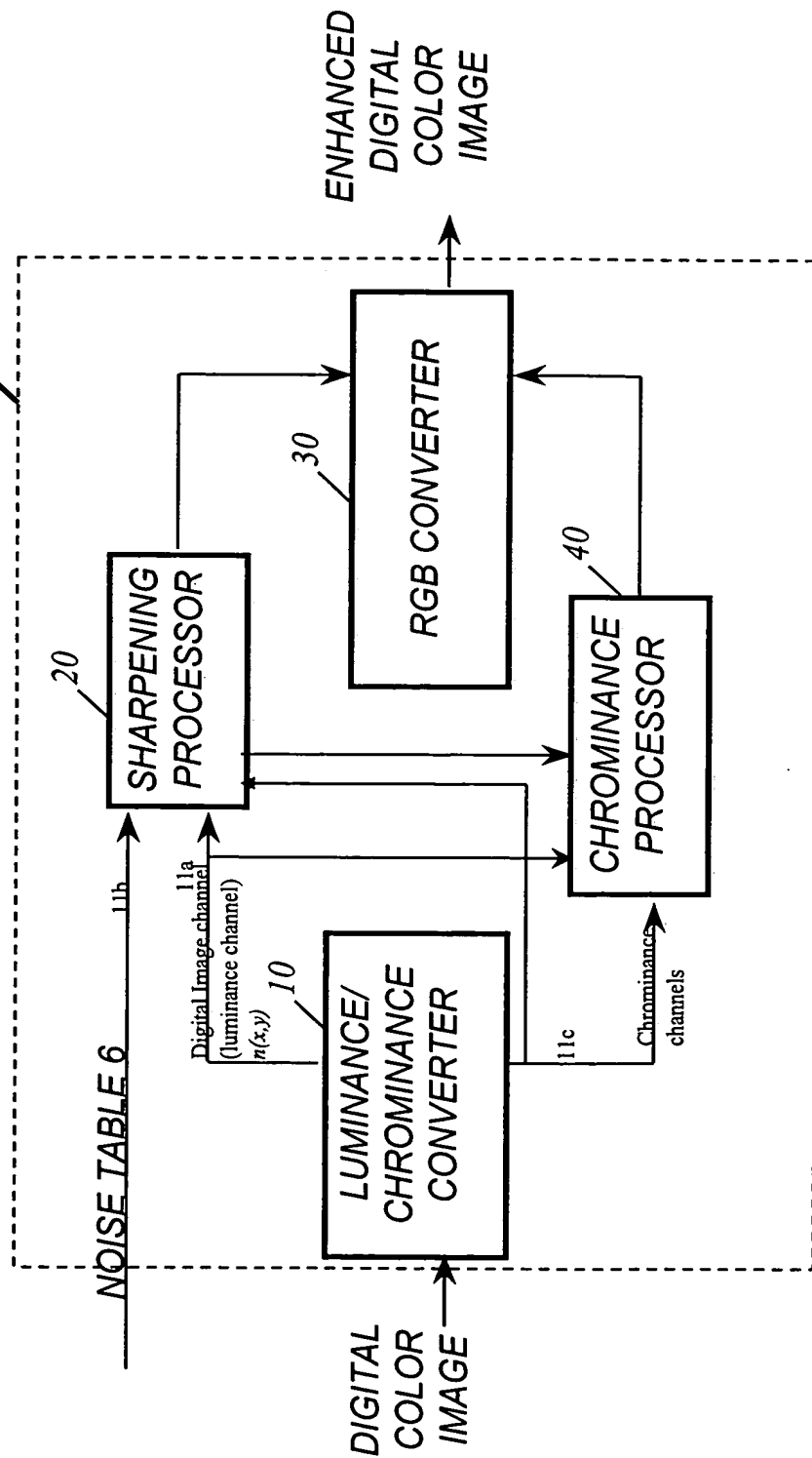
(system diagram)

FIG. 1



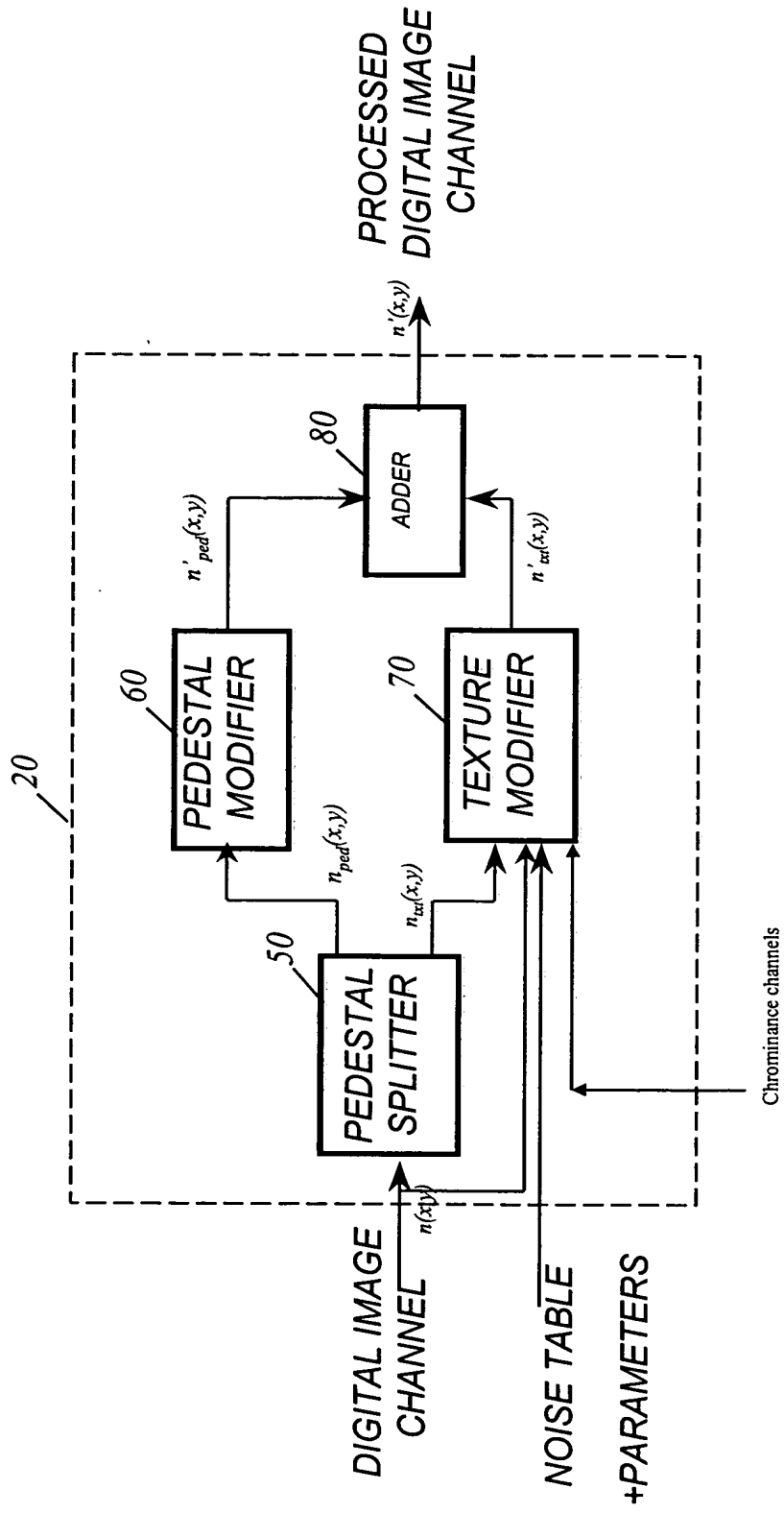
(system diagram)

FIG. 2



(sharpening processor 20)

FIG. 3



(sharpening processor 20)

FIG. 4

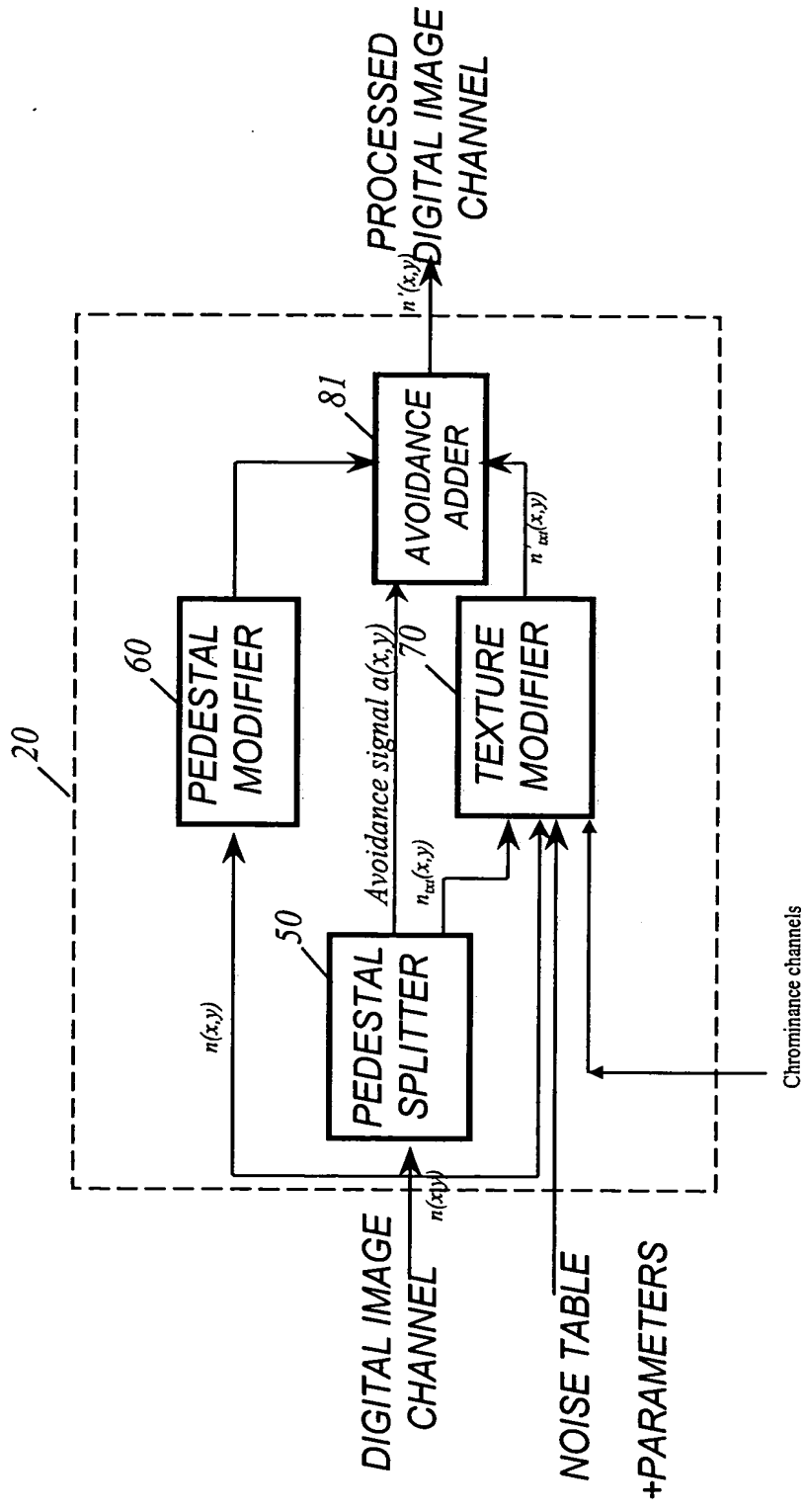


FIG. 5
(PEDESTAL SPLITTER)

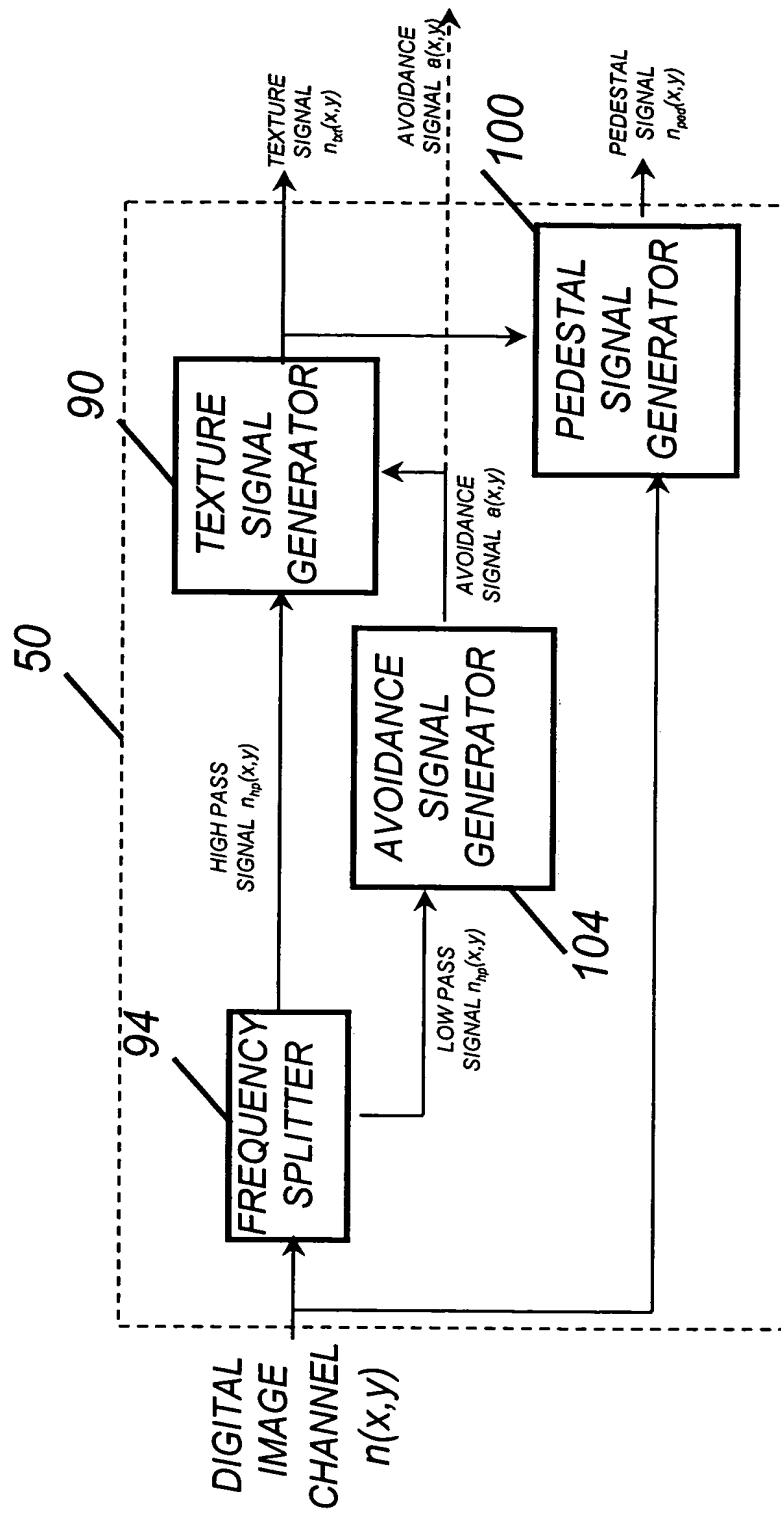


FIG. 5A
(ALTERNATIVE EMBODIMENT OF
THE PEDESTAL SPLITTER)

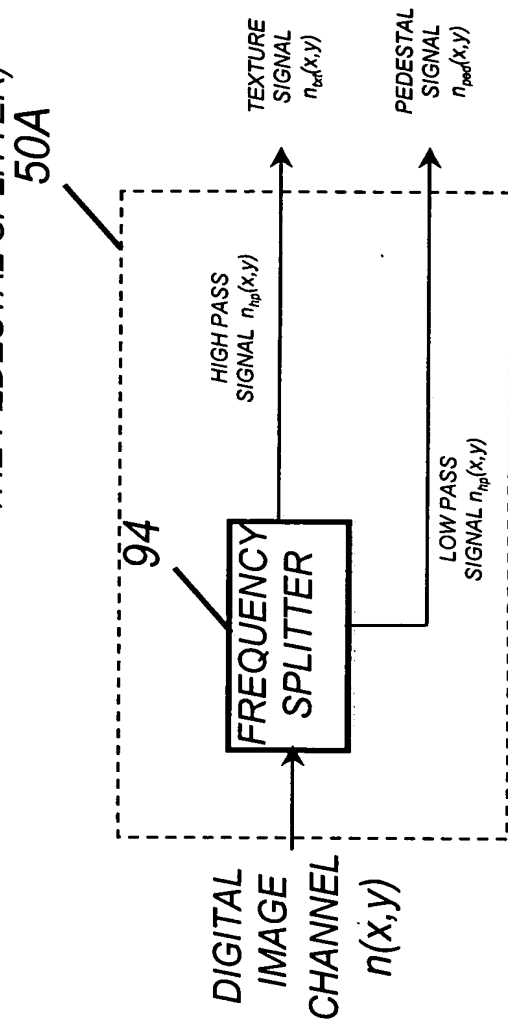


FIG. 5B
(ALTERNATIVE EMBODIMENT OF
THE PEDESTAL SPLITTER)

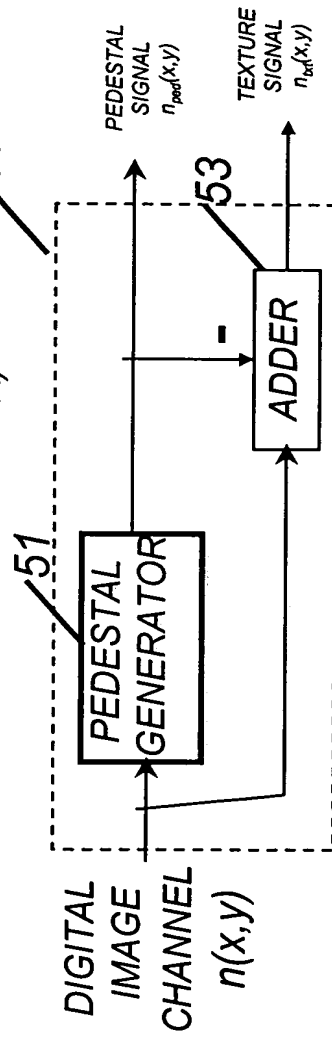
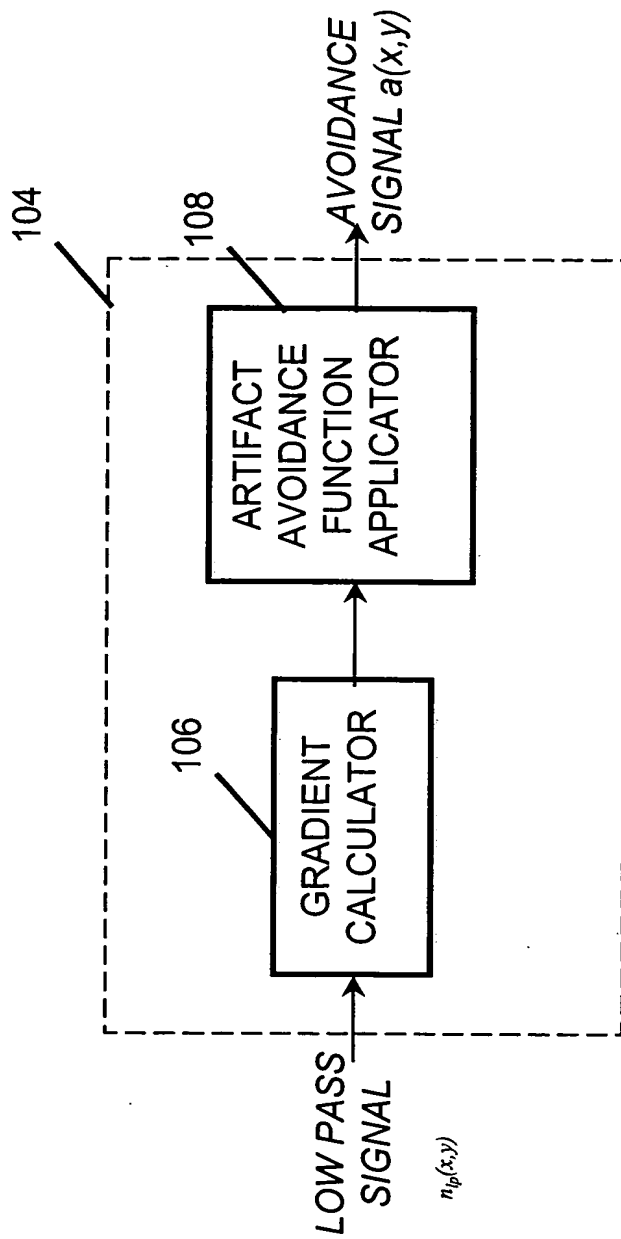
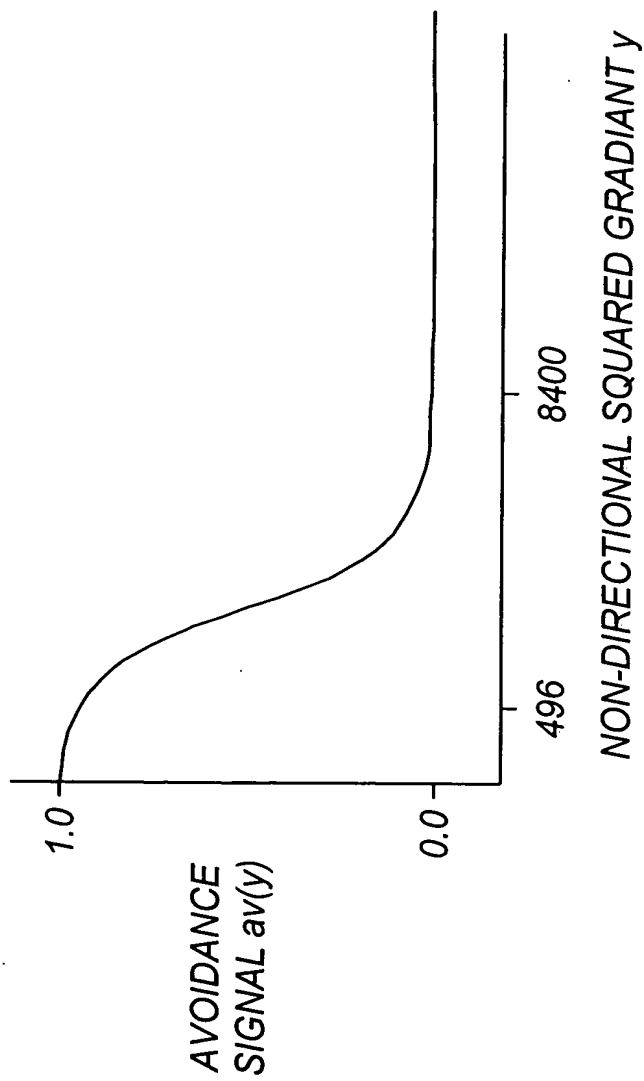


FIG. 6 (AVOIDANCE SIGNAL GENERATOR)



(ARTIFACT AVOIDANCE FUNCTION)

FIG. 7



(PEDESTAL MODIFIER 60)

FIG. 8

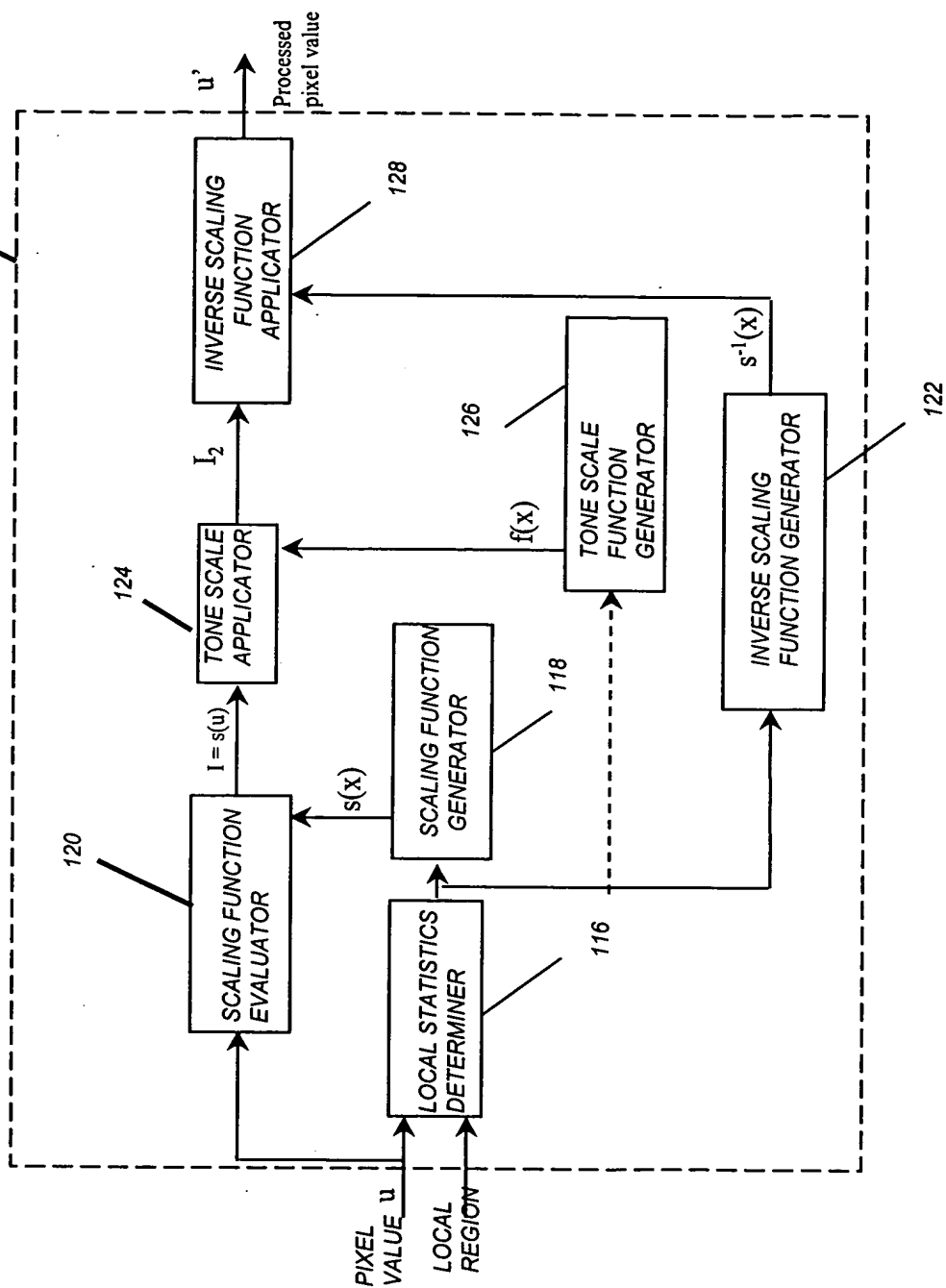


FIG. 9 is a schematic diagram of a grid structure 110, 112, 114.

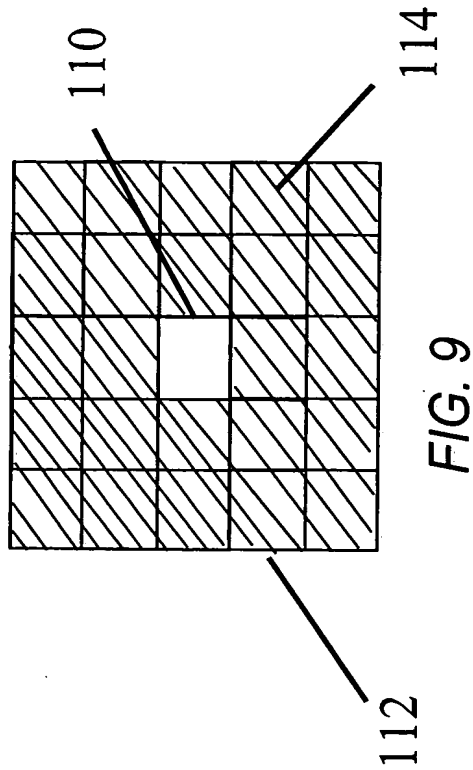


FIG. 9

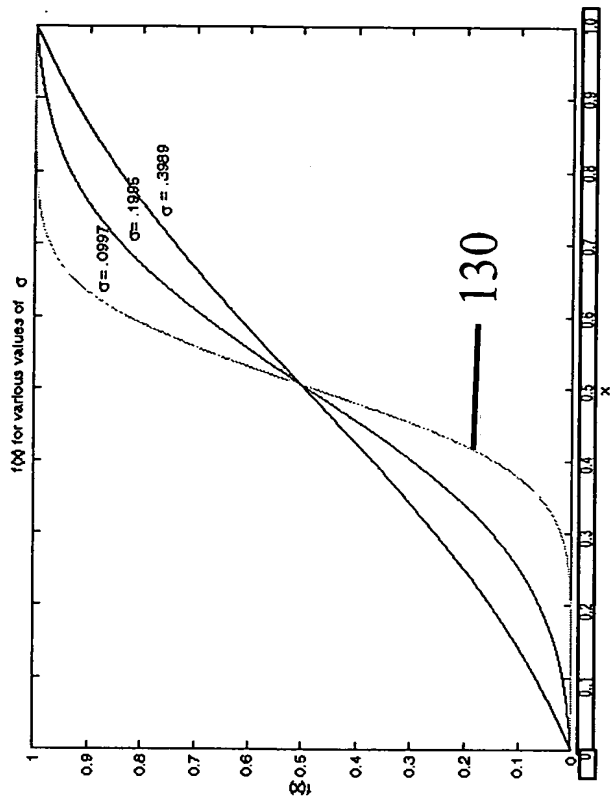


FIG. 10

FIG. 11

(texture modifier 70)

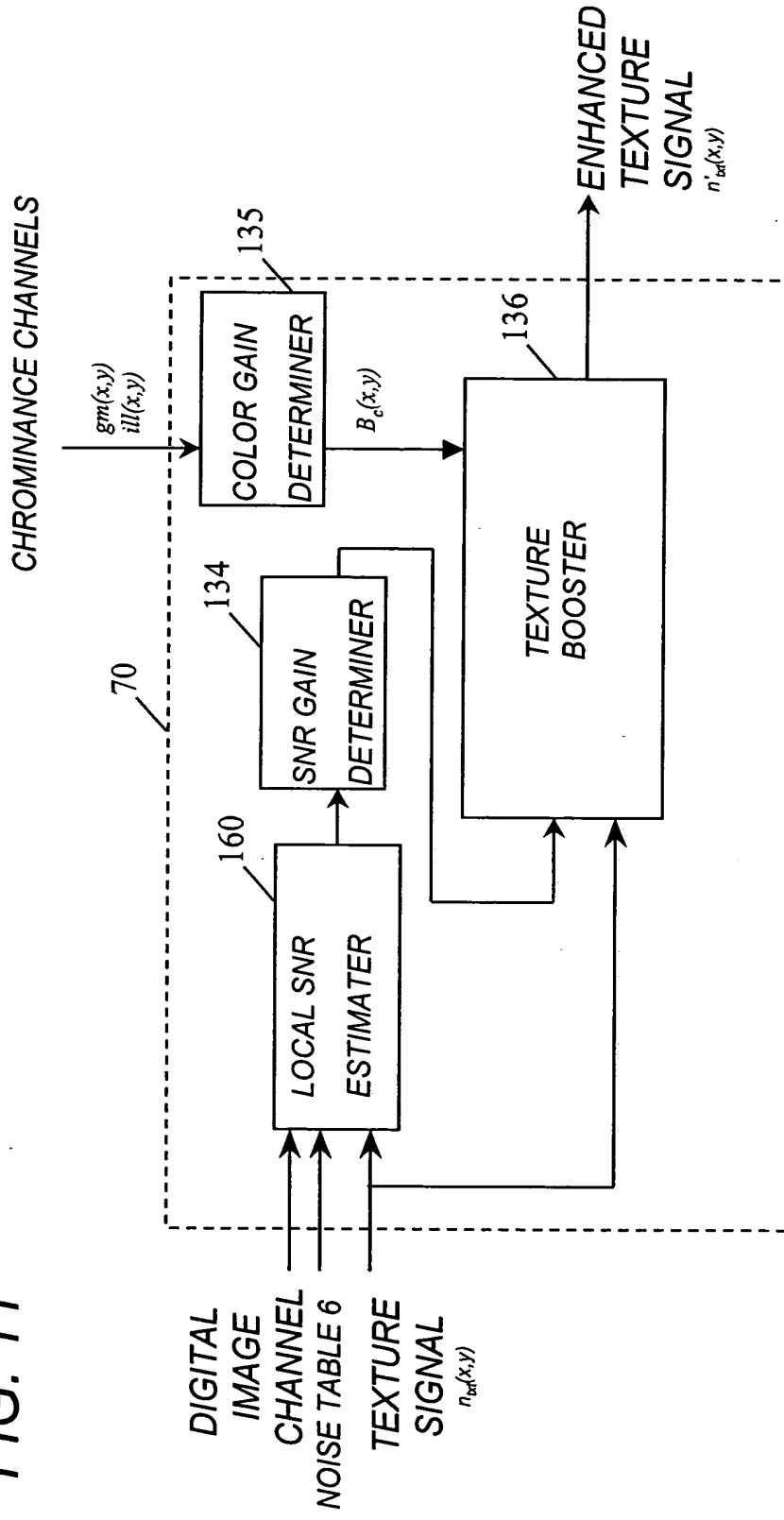


FIG. 12

(LOCAL SNR ESTIMATOR 115)

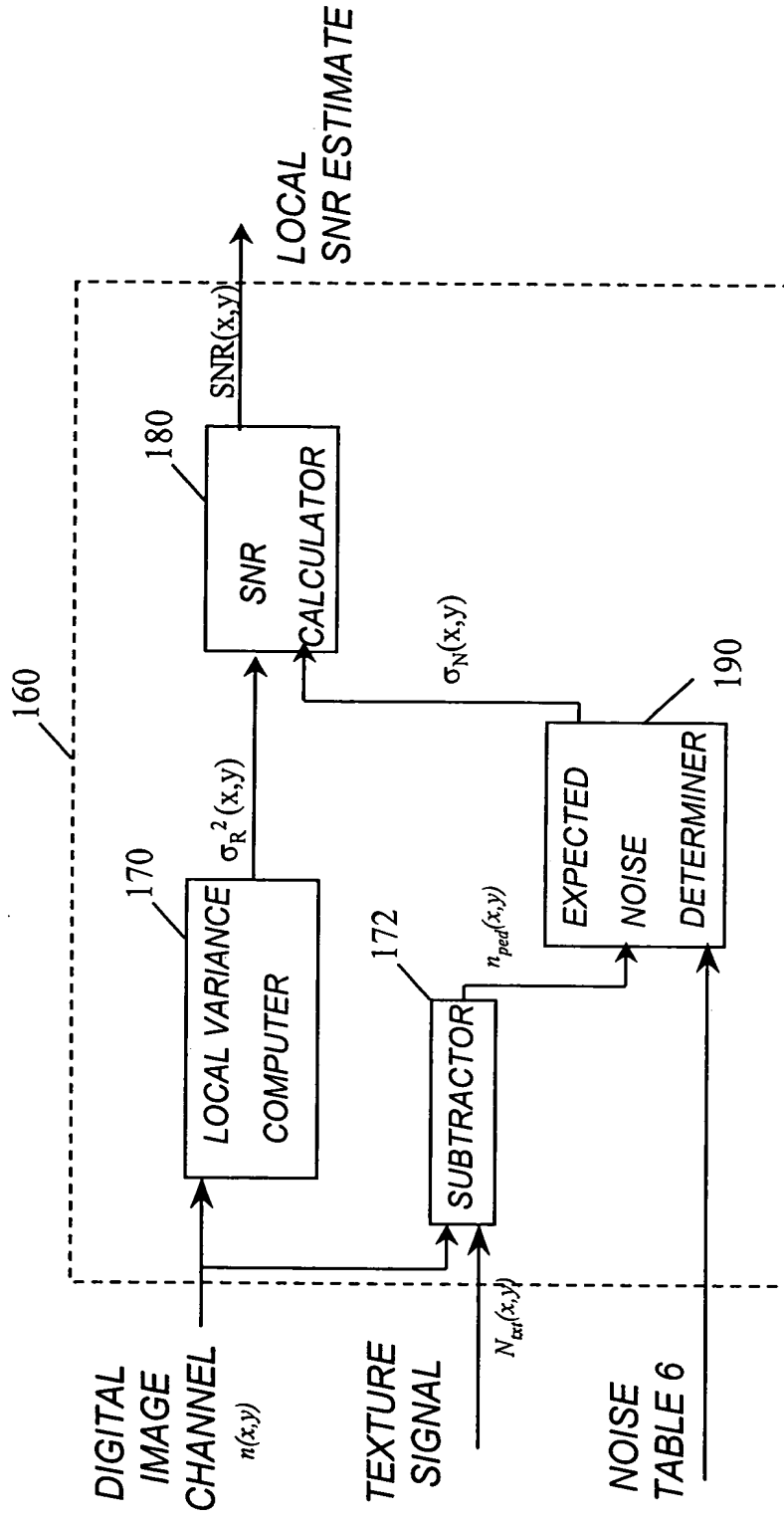
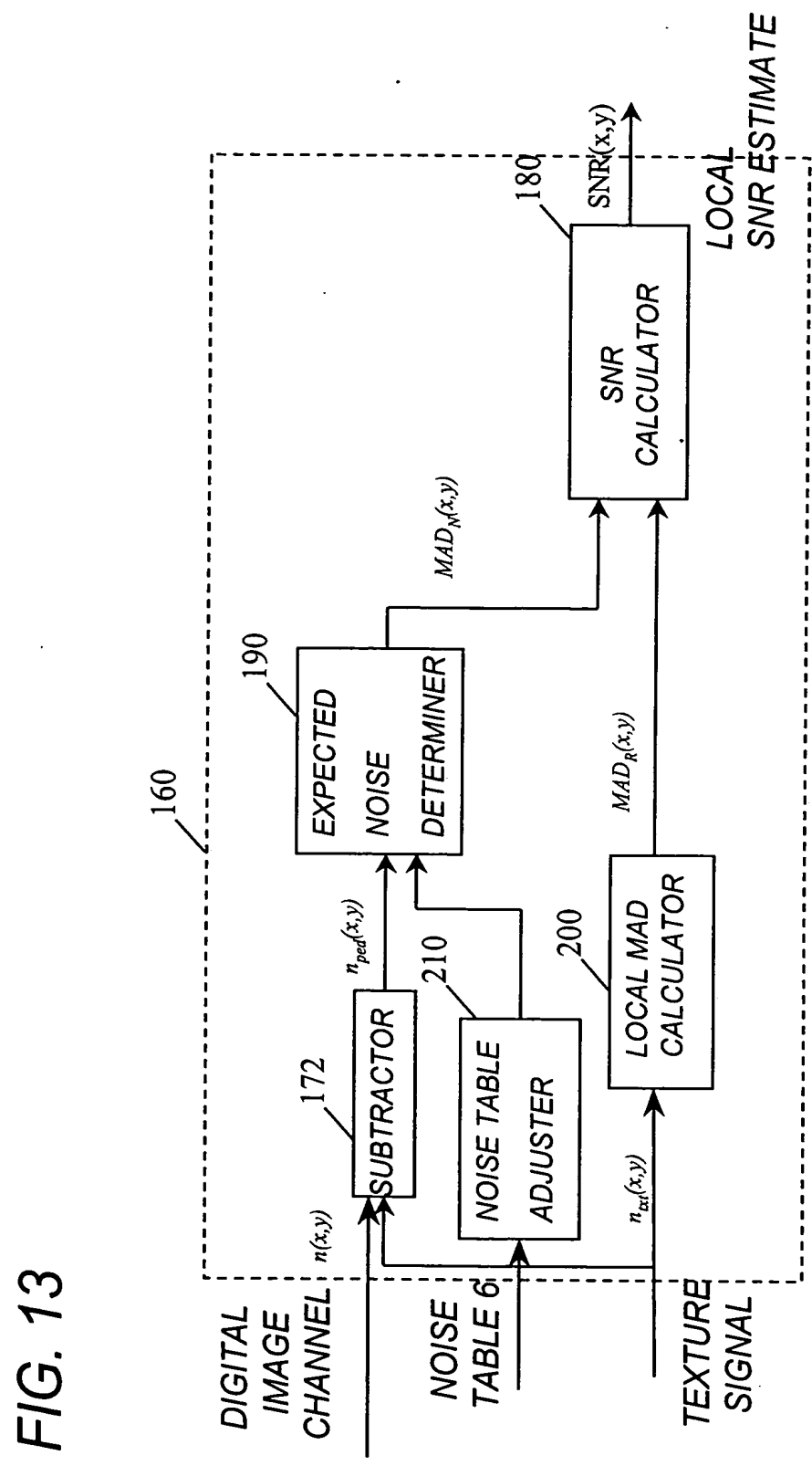


FIG. 13
(ALTERNATIVE EMBODIMENT OF LOCAL SNR ESTIMATOR 160)



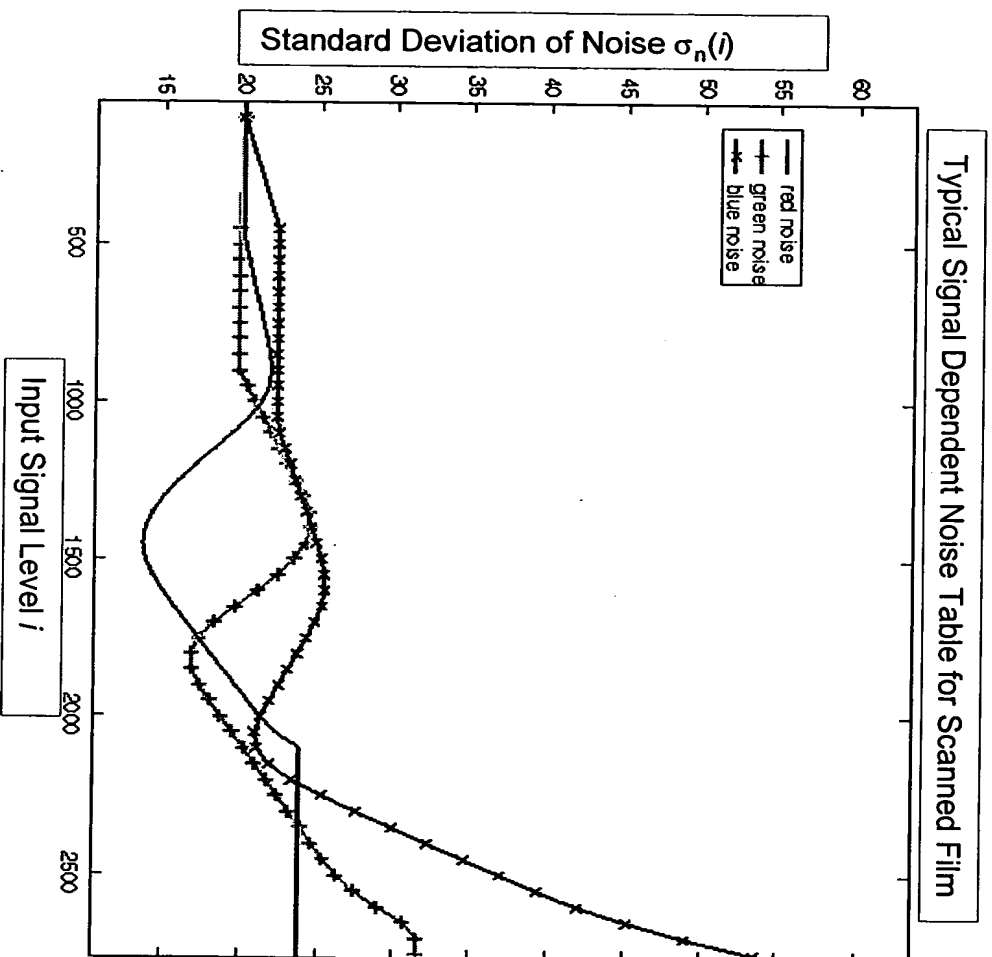


Fig. 14

Conversion function in SNR Gain determiner 134

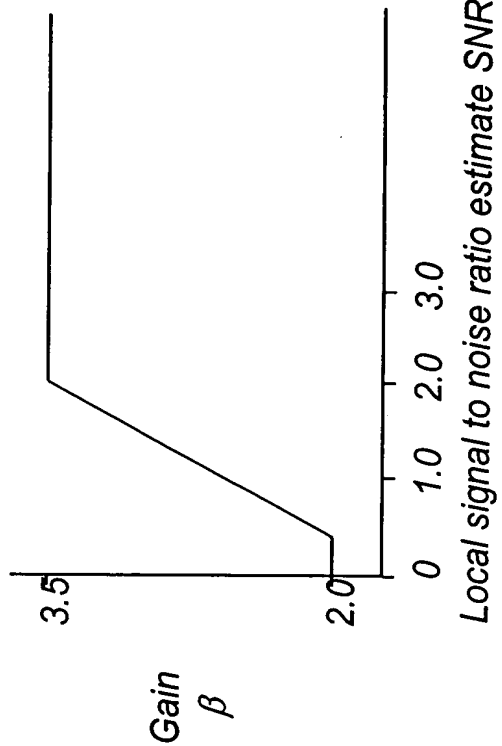


FIG. 15A

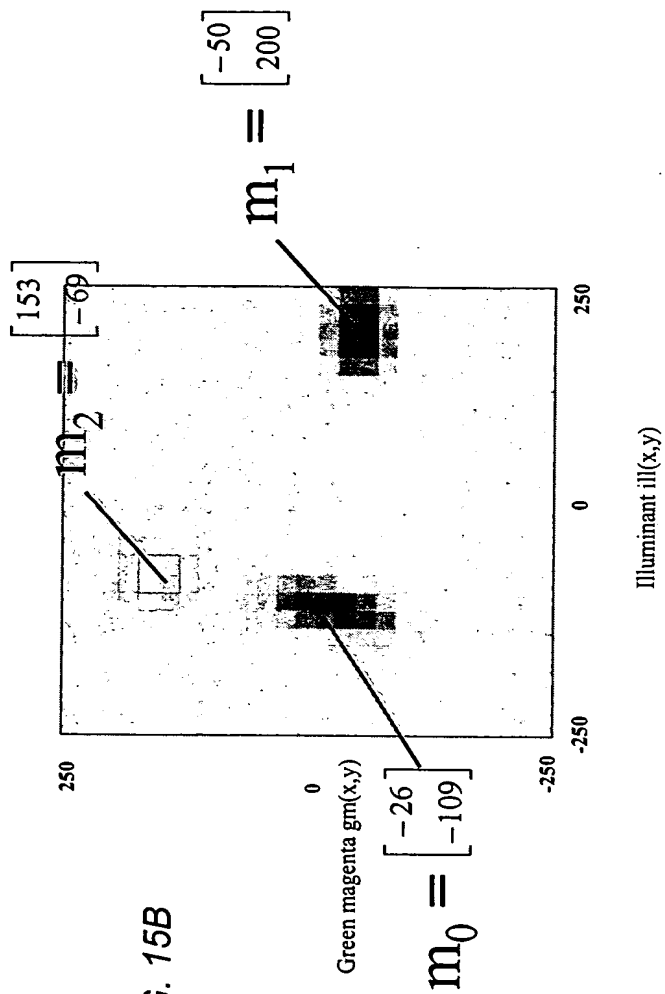


FIG. 15B